

# 7.00mm (0.28 inch) Pure Green LED Display Single Digit 7-segment Surface Mount LED Display

#### **Technical Data Sheet**

#### **Features**

- High reliability.
- Low power consumption.
- Excellent characters appearance.
- Evenly lighted segments.
- Packaged in tape and reel for SMT manufacturing.
- The thickness is thinness than tradition through-hole LED display.
- I.C compatible.
- RoHS compliant.



# **Descriptions**

- The KW1-S282CGA-G5 is a 7.00mm (0.28inch) digit height surface mount 7-segment LED display.
- The display provides excellent reliability in bright ambient light.
- The device is available as either common anode or common cathode.
- The device is made with white diffused segments and gray surface.

### **Applications**

- Home and smart appliances.
- Instrument panels.
- Display time and digital combination.
- Test and measurement equipment.
- Control units.

#### **Device Selection Guide**

| Part No.       | <b>Emitting Color</b> | Circuit Common |  |
|----------------|-----------------------|----------------|--|
| KW1-S282CGA-G5 | Pure Green            | Common Cathode |  |

Spec No.: Z-KW1-S282-MR

Issue No.: G-001-Rev-3

Luckylight Electronics Co., Ltd

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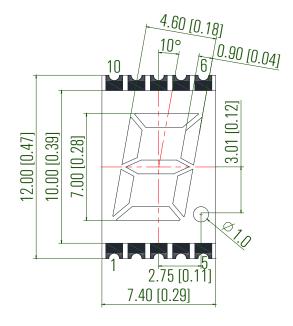
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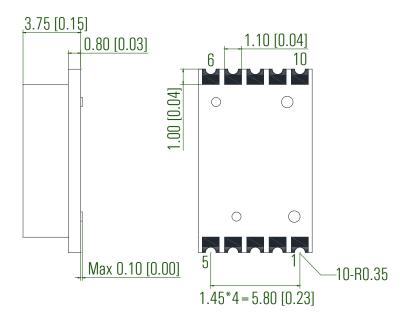


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# **Package Dimension**









#### Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is  $\pm$  0.25 mm (.010") unless otherwise noted.
- 3. The gap between the reflector and PCB shall not exceed 0.25mm.

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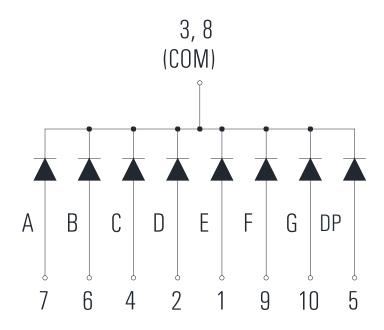
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# **Internal Circuit Diagram:**





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# Absolute Maximum Ratings at Ta=25°C

| Parameters   | Symbol          | Max                 | Unit |  |
|--|-----------------|---------------------|------|--|
| Power Dissipation (Per Chip)   | $P_{d}$         | 64                  | mW   |  |
| Peak Forward Current (Per Segment)<br>(1/10 Duty Cycle, 0.1ms pulse width) | I <sub>FP</sub> | 40                  | mA   |  |
| Forward Current (Per Segment)  | I <sub>F</sub>  | 20                  | mA   |  |
| Reverse Voltage (Per Chip)   | $V_R$           | 5                   | V    |  |
| Operating Temperature Range  | $T_{opr}$       | -40°C to +80°C      |      |  |
| Storage Temperature Range  | $T_{stg}$       | -40°C to +85°C      |      |  |
| Soldering Temperature  | $T_{sld}$       | 260°C for 5 Seconds |      |  |

# Electrical Optical Characteristics at Ta=25°C

| Parameters                        | Symbol              | Min. | Тур. | Max. | Unit | Test Condition   |
|-----------------------------------|---------------------|------|------|------|------|------------------|
| Average Luminous Intensity        | lv -                | 6.25 | 12.5 |      | mcd  | IF=5mA (Note a)  |
|                                   |                     | 12.5 | 25.0 |      | mcd  | IF=10mA (Note a) |
| Luminous Intensity Matching Ratio | $I_{v-m}$           |      |      | 2:1  |      | IF=20mA          |
| Peak Emission Wavelength          | λр                  |      | 520  |      | nm   | IF=20mA          |
| Dominant Wavelength               | λd                  |      | 525  |      | nm   | IF=20mA (Note b) |
| Spectral Line Half-Width          | $\triangle \lambda$ |      | 35   |      | nm   | IF=20mA          |
| Forward Voltage (Per Segment)     | V <sub>F</sub>      |      | 3.0  | 3.2  | V    | IF=20mA (Note c) |
| Reverse Current (Per Segment)     | $I_R$               |      |      | 50   | μΑ   | VR=5V            |

#### Notes:

- a. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve. Tolerance of Luminous Intensity:  $\pm$  10 %
- b. The dominant wavelength ( $\lambda d$ ) is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.
- c. Tolerance of Forward Voltage: ± 0.1V

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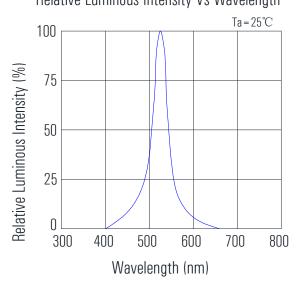


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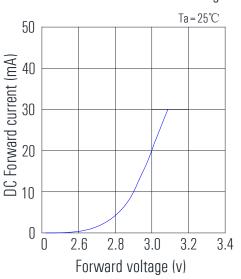
# **Technical Data Sheet**

# **Typical Electrical/Optical Characteristics Curves** (25°C Ambient Temperature Unless Otherwise)

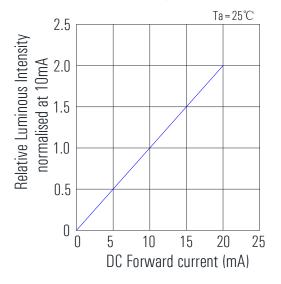
Relative Luminous Intensity Vs Wavelength



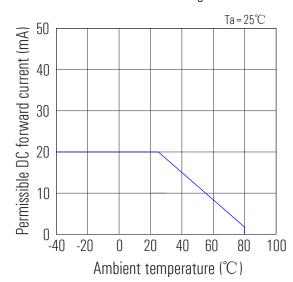
Forward Current vs Forward Voltage



Relative Luminous Intensity vs Forward Current



Forward Current Derating Curve



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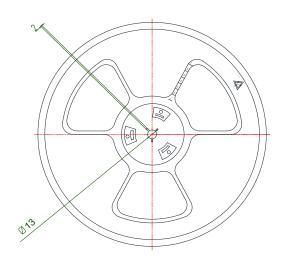
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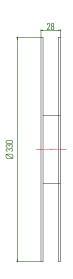


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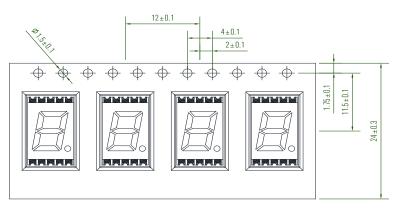
# Reel Dimensions (units: mm)

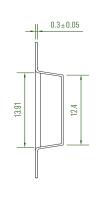


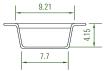


# **Tape Specifications** (units: mm)

# Progressive direction







Note: Tolerances unless mentioned  $\pm 0.25 mm$ .

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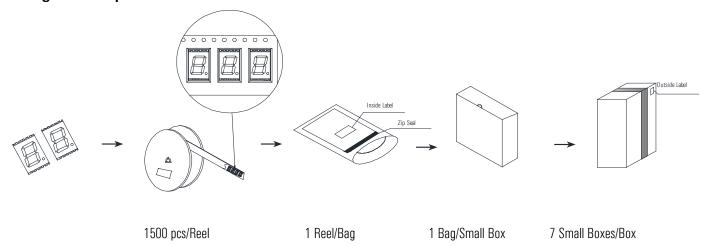
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# **Technical Data Sheet**

# **Packing & Label Specifications**

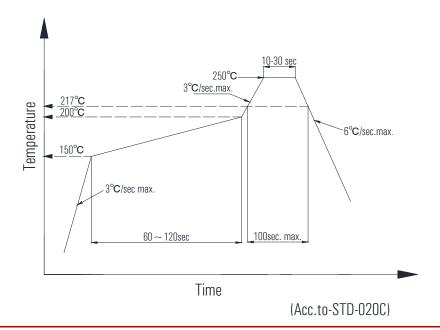


#### **Precautions for Use**

#### 1. Caution in ESD

Static electricity and surge damages the LED. It is recommended to use a wrist band or anti-electrostatic glove when handling the LED. All devices equipment and machinery must be properly grounded.

### 2. Pb-Free Reflow Soldering Profile



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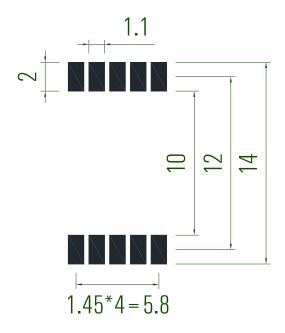
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#### Notes:

| Pre-heat             |               | Other                                 |                |
|----------------------|---------------|---------------------------------------|----------------|
| Temperature min      | 150℃          | Liquidus Temperature                  | 217℃           |
| Temperature max      | 200 °C        | Time above Liquidus Temperature       | 100sec.max     |
| Time                 | 60-120sec     | Peak Temperature                      | 250℃           |
| Average ramp-up rate | 3 °C/sec.max. | Ramp- Down Rate from Peak Temperature | 6 °C/sec. max. |
|                      |               | Reflow times                          | 1 time         |

- a. Reflow soldering is recommended. Other soldering methods are not recommended as they might cause damage to the product.
- b. All parameters are maximum body case temperature values and cannot be considered as a soldering profile. The body case temperature was measured by soldering a thermal couple to the soldering point of LEDs.
- c. No more than one reflow welding is recommended.

### 3. Recommended Soldering Pattern



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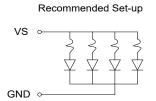


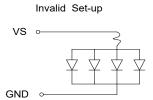
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#### 4. Circuit Design Notes:

- a. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
- b. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.
- c. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
- d. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.





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- 6. The LEDs should be operated with forward bias. The driving circuit must be designed so that the LEDs are not subjected to forward or reverse voltage while it is off. If reverse voltage is continuously applied to the LEDs, it may cause migration resulting in LED damage.
- 7. Sundries and stains are controlled within 0.2mm, which can be passed.