0.56 inch (14.20mm), White Single Digit 7-segment LED Display

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Features:

- 0.56" (inch) digit height
- Low power consumption
- High reliability
- Excellent characters appearance
- Available in common cathode or common anode
- RoHS Compliant



Descriptions:

- The KW1-563AWB/KW1-563CWB is a 0.56 inch (14.20mm) height single digit display.
- The display provides excellent reliability in bright ambient light.
- The device is made with white segments and black surface.

Applications:

- Home appliances
- Instrument panels
- Digital readout displays
- Game console systems

Device Selection Guide:

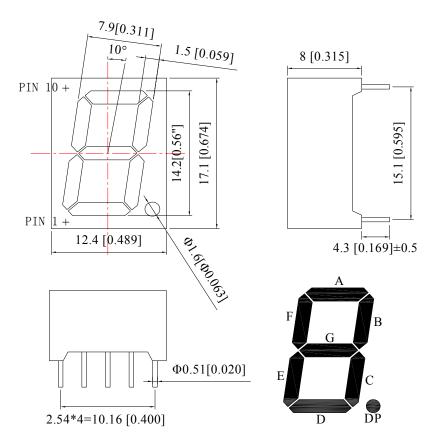
Part No.	Emitting Color	Pola	rity
KW1-563AWB	White	Commo	n Anode
KW1-563CWB	White	Common	Cathode
Spec No.: W5618A/B		Date:	16-Mar-2017
Issue No.: G-001-Rev-3		E-mail:	sales@luckylight.cn
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Package Dimension:



Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is \pm 0.25 mm (.010") unless otherwise noted.

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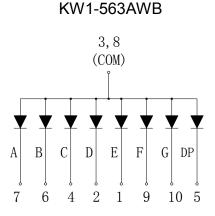
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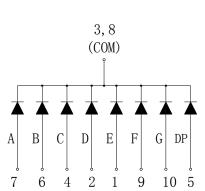
Internal Circuit Diagram:

Internal Circuit Diagram (Common Anode)



Internal Circuit Diagram (Common Cathode)

KW1-563CWB



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Absolute Maximum Ratings at Ta=25°C

Parameters	Symbol	Мах	Unit	
Power Dissipation Per Segment	P _d	35	mW	
Peak Forward Current Per Segment (1/10 Duty Cycle, 0.1ms Pulse Width)	I _{FP}	50 mA		
Forward Current Per Segment	I _F	10	mA	
Reverse Voltage Per Segment	V _R	5 V		
Operating Temperature Range	T _{opr}	-40°C to +80°C		
Storage Temperature Range	T _{stg}	-40℃ to +85℃		
Soldering Temperature	T _{sld}	$260^\circ\!\mathrm{C}$ for 5 Seconds		

Electrical Optical Characteristics at Ta=25°C

Parameters	Symbol	Min.	Тур.	Max.	Unit	Test Condition
Average Luminous Intensity	lv	65	130		mcd	IF=10mA (Note 1, 2)
Luminous Intensity Matching Ratio	I _{v-m}			2:1		IF=10mA
Chromoticity Coordinates	х		0.30			
Chromaticity Coordinates	У		0.31			IF=10mA (Note 3)
Forward Voltage Per Segment	V _F		2.9	3.10	V	IF=10mA
Reverse Current Per Segment	I _R			50	μA	VR=5V

Notes:

- 1. Luminous Intensity is a average value which is measured one 7-segment. Tolerance of Luminous Intensity: ±10%.
- 2. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.
- 3. The chromaticity coordinates (x, y) is derived from the 1931 CIE chromaticity diagram.

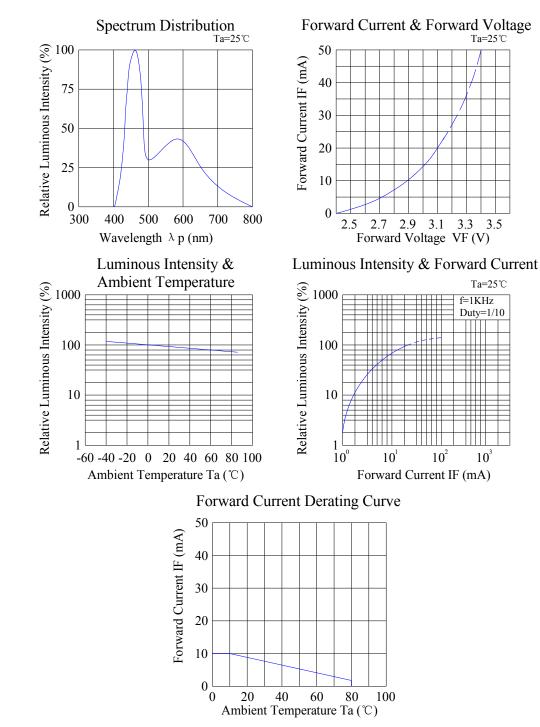
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Typical Electrical / Optical Characteristics Curves (25°C Ambient Temperature Unless Otherwise Noted)



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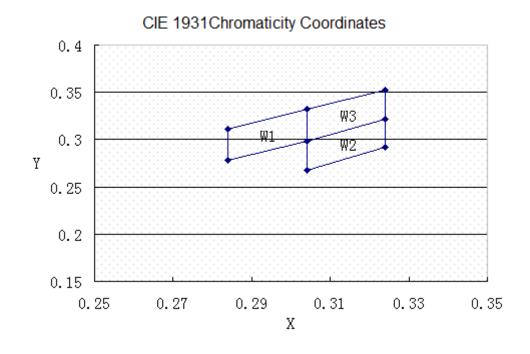
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Chromaticity Coordinates Specifications for Bin Rank

Bin Code	CIE 1931Chromaticity Coordinates				
W1	Х	0.284	0.284	0.304	0.304
VVI	у	0.278	0.311	0.332	0.298
W2	Х	0.304	0.304	0.324	0.324
VVZ	у	0.268	0.298	0.322	0.292
W3	Х	0.304	0.304	0.324	0.324
vv3	у	0.298	0.332	0.352	0.322

Color Bin at IF = 10mA

Tolerance on each Hue (x, y) bin is +/- 0.01.



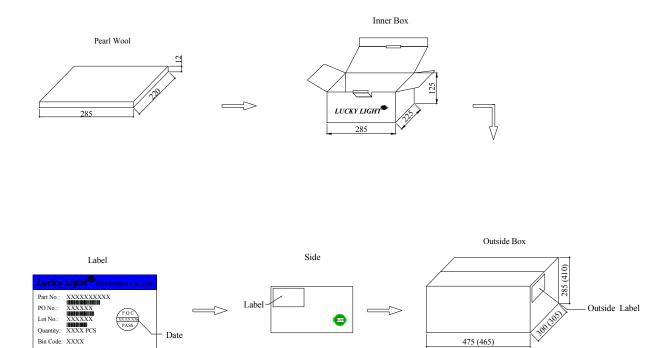
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Packing & Label Specifications:



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- f. Over-current-proof
- Customer must apply resistors for protection, otherwise slight voltage shift will cause big current change (Burn out will happen).
- g. Storage
- 1. Before opening the package, the LEDs should be kept at 30° Cor less and 80%RH or less.
- 2. The LEDs should be used within a year.
- 3. After opening the package, the LEDs should be kept at 30 $^\circ\!\mathrm{Cor}$ less and 60%RH or less.
- h. ESD (Electrostatic Discharge)
 - Static Electricity or power surge will damage the LED. Suggestions to prevent ESD damage:
 - IUse of a conductive wrist band or anti-electrostatic glove when handling these LEDs.
 - IAll devices, equipment, and machinery must be properly grounded.

Work tables, storage racks, etc. should be properly grounded.

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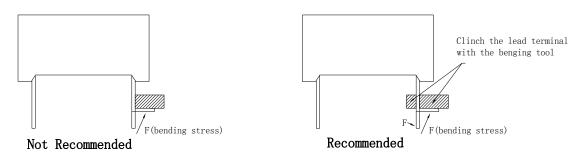
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Through Hole Display Mounting Method

Lead Forming:

- 1. Do not bend the component leads by hand without proper tools.
- 2. The leads should be bent by clinching the upper part of the lead firmly such that the bending force Is not exerted on the plastic body.

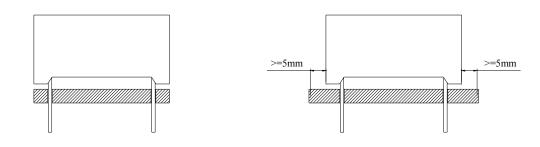


Installation:

- 1. The installation process should not apply stress to the lead terminals.
- 2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.



3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.

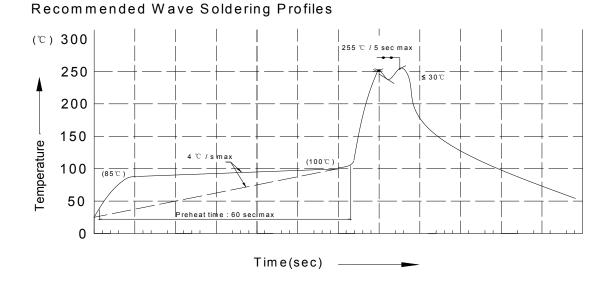


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Notes:

- 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C.
- 2. Peak wave soldering temperature between $245^{\circ}C \sim 255^{\circ}C$ for 3 sec (5 sec max).
- 3. Do not apply stress to the epoxy resin while the temperature is above 85° C.
- 4. Fixtures should not incur stress on the component when mounting and during soldering process.
- 5. SAC 305 solder alloy is recommended.
- 6. No more than one wave soldering pass.
- 7. During wave soldering, the PCB top-surface temperature should be kept below $105\,^\circ\!\!\mathbb{C}_{\,\cdot}$.

Soldering General Notes:

- 1. Through-hole displays are incompatible with reflow soldering.
- 2. If components will undergo multiple soldering processes, or other processes where the omponents may be subjected to intense heat, please check with luckylight for compatibility.

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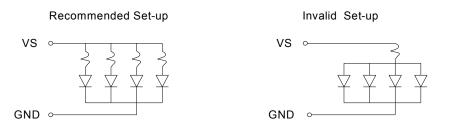
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Cleaning:

- 1. Mild "no-clean" fluxes are recommended for use in soldering.
- 2. If cleaning is required, luckylight recommends to wash components with water only.
- Do not use harsh organic solvents for cleaning because they may damage the plastic parts.
- 3. The cleaning process should take place at room temperature and the devices should not be washed for more than one minute.
- 4. When water is used in the cleaning process, immediately remove excess moisture from the component with forced-air drying afterwards.

Circuit Design Notes:

- 1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
- 2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.



- 3. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
- 4. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.
- 5. Prolonged reverse bias should be avoided, as it could cause metal migration, leading to an increase in leakage current or causing a short circuit.

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