

FV4818RGBW2-F1S-6P

4.8x1.8mm, Intelligent control LED

PLCC-6 Side Package integrated light source



Technical Data Sheet

Features:

FV4818 Sede is a smart LED control circuit and light emitting circuit in one controlled LED source, which has the shape of a 4818 LED chip. Each lighting element is a pixel, and the intensities of the pixels are contained within the intelligent digital interface input. The output is driven by patented PWM technology, which effectively guarantees high consistency of the color of the pixels. The control circuit consists of a signal shaping amplification circuit, a built-in constant current circuit, and a high precision RC oscillator.

The data protocol being used is unipolar NRZ communication mode. The 24-bit data is transmitted from the controller to DIN of the first element, and if it is accepted it is extracted pixel to pixel. After an internal data latch, the remaining data is passed through the internal amplification circuit and sent out on the DO port to the remaining pixels. The pixel is reset after the end of DIN. Using automatic shaping forwarding technology makes the number of cascaded pixels without signal transmission only limited by signal transmission speed.

The LED has a low driving voltage (which allows for environmental protection and energy saving), high brightness, scattering angle, good consistency, low power, and long life. The control circuit is integrated in the LED above.

Applications:

- Full-color module, Full color soft lights a lamp strip.
- LED decorative lighting, Indoor/outdoor LED video irregular screen.

Part No.	Emitting Color		Lens Color
FV4818RGBW2-F1S-6P	R	Red	Water Clear
	G	Pure Green	
	B	Blue	
	W	White	Yellow Diffused

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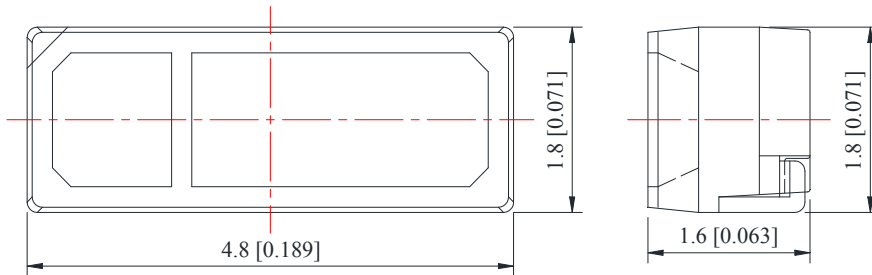
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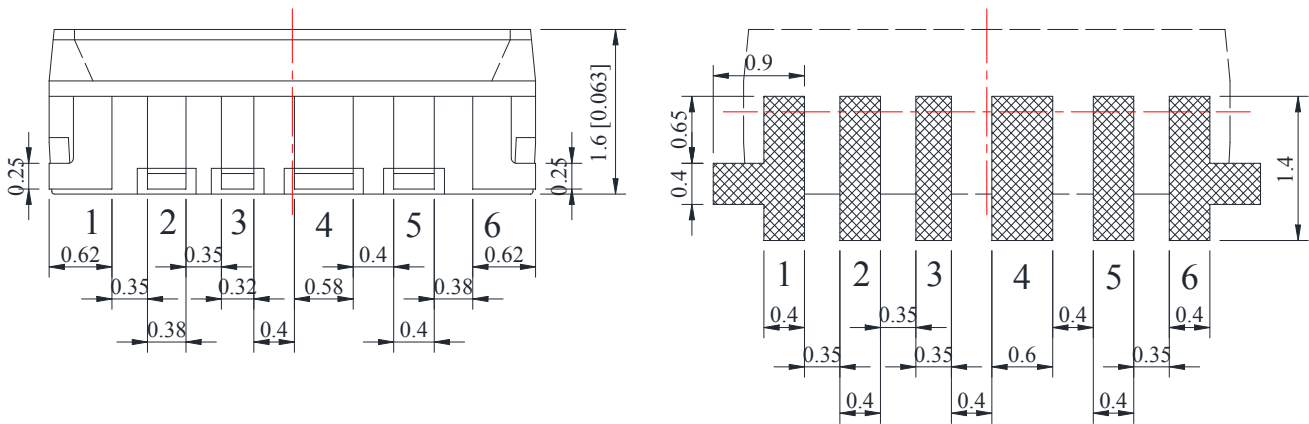
Package Dimension:



Pin Function

- 1 W-VDD: Power Supply White LED
- 2 NC: No Connect
- 3 DOUT: Control Data Signal Output
- 4 GND: Ground
- 5 VDD: Power Supply LED
- 6 DIN: Control Data Signal Input

Recommended Soldering Pad Dimensions



PIN function:

NO.	Symbol	Function description
1W	VDD	Power supply LED
2	NC	NC
3	DOUT	Control data signal output
4	GND	Ground
5	VDD	Power supply LED
6	DIN	Control data signal input

Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ± 0.25 mm (.010") unless otherwise noted.
- 3. Protruded resin under flange is 1.00mm (.039") max.

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Absolute Maximum Ratings at Ta=25°C

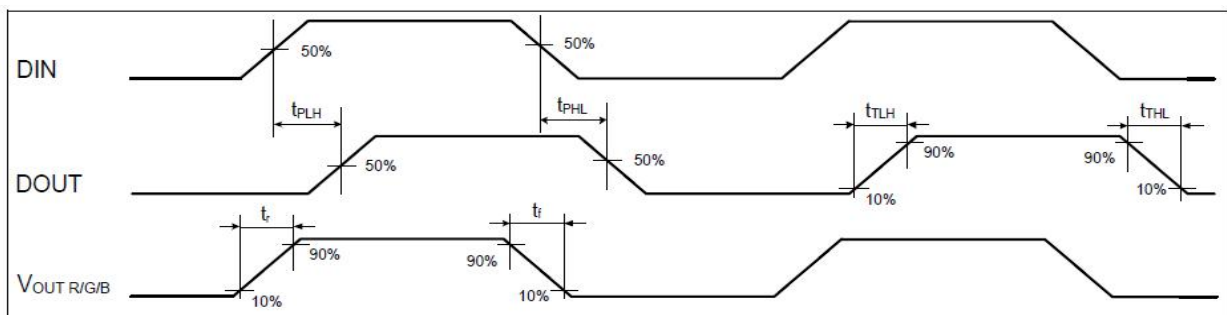
Parameter	Symbol	Ratings	Unit
Power supply voltage	VDD	+3.5~+5.5	V
Input voltage	VI	-0.5~VDD+0.5	V
Operation junction temperature	Topt	-40~+80	°C
Storage temperature range	Tstg	-40~+85	°C

Electrical Characteristics: (TA=-20~+70°C, VDD=4.5~5.5V, VSS=0V, unless otherwise specified)

Parameter	Symbol	conditions	Min	Tpy	Max	Unit
The chip supply voltage	VDD	—	—	5.2	—	v
The frequency of PWM	FPWM	—	—	1.2	—	KHZ
Static power consumption	IDD	---	—	1	---	MA
The signal input flip threshold	VIH	VDD=5.0V	0.70	—	—	V
	VIL	VDD=5.0V	—	—	0.30	

Switching characteristics:

Parameter	Symbol	Condition	Min	Tpy	Max	Unit
The speed of data transmission	fDIN	The duty ratio of 67% (data 1)	—	800	—	KHZ
IOOUT Rise/Drop Time	Tr	VDS=1.5	—	100	—	ns
			—	100	—	
DOOUT transmission delay	TPLH	DIN→DOOUT	—	—	500	ns
	TPHL		—	—	500	



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RGB IC characteristic parameter:

Emitting color	Model	Wavelength(nm)	Luminous intensity (mcd) Typ
Red	—	620-625	350
Green	—	520-525	1000
Blue	—	465-470	250
White		5500-10000	2500

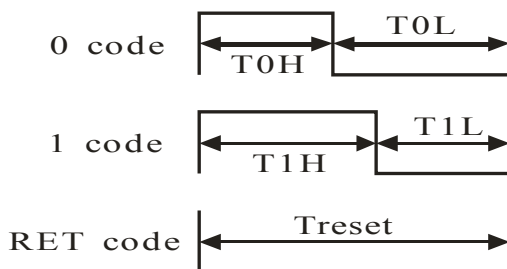
Data transfer time:

Timetable name	Min.	Tpy	Max	Unit	
T	Meta code cycle	1.20	--	us	
T0H	0 code ,high voltage time	0.2	0.32	0.4	us
T0L	0 code , low voltage time	0.8	--	--	us
T1H	1 code ,high voltage time	0.55	0.64	1.05	us
T1L	1 code ,low voltage time	0.2	--	--	us
RES	low voltage time	>80	--	--	us

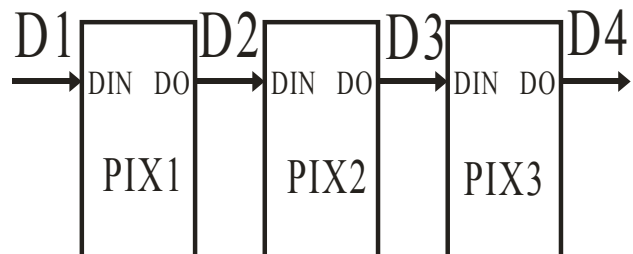
Notes:

- 1.The protocol uses unipolar zero-return codes. Each symbol must have a low level. Each symbol in this protocol starts at a high level. The width of high level time decides "0" code or "1" code.
- 2.When writing a program, the minimum requirement for symbol period is 1.2 us.
- 3.The high-level time of "0" code and "1" code should be in accordance with the range specified in the table above, and the low-level time of "0" code and "1" code should be less than 20 us.

Sequence chart:



Cascade method:



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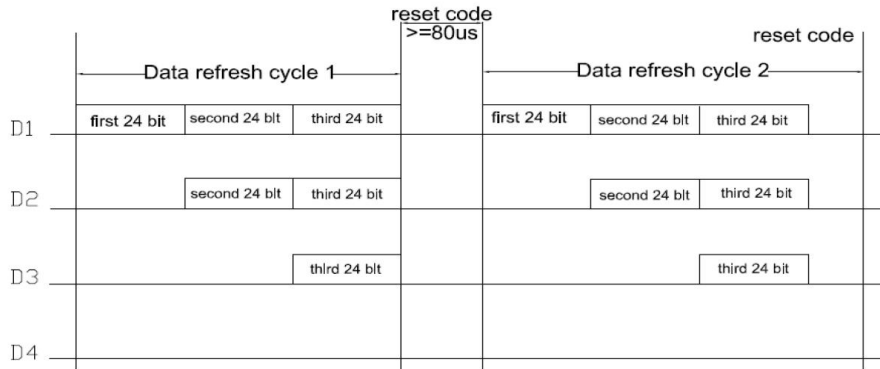
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Data transmission method:



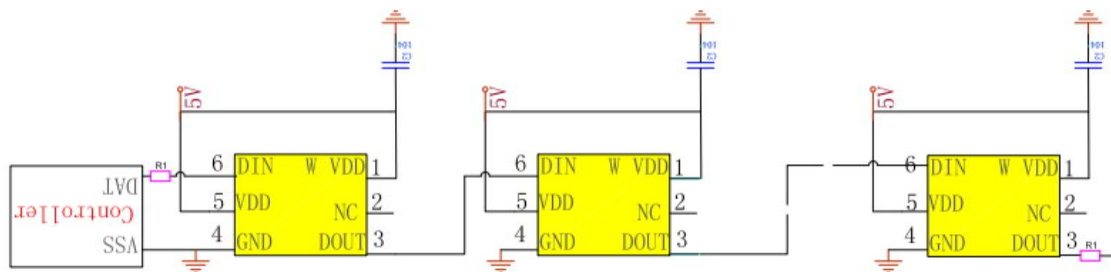
Note: The data of D1 is send by MCU, and D2, D3, D4 through pixel internal reshaping amplification to transmit.

Composition of 24bit data:

G7	G6	G5	G4	G3	G2	G1	G0	R7	R6	R5	R4	R3	R2	R1	R0	B7	B6	B5	B4	B3	B2	B1	B0
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Note: Follow the order of GRB to sent data and the high bit sent at first.

Typical application circuit:



Note:

In practical application circuit, in order to prevent the instantaneous high voltage damage of IC internal signal input and output pins caused by live plug-in during testing, signal input should be used.

The protective resistor is connected in series with the output terminal. In addition, in order to make the IC chips work more stably, the decoupling capacitance between the lamp beads is indispensable.

Application 1: For soft lamp or hard lamp, the transmission distance between lamp beads is short. It is suggested that the protective resistors should be connected in series at the input and output of signal and clock line. $R1 = R0$ about 500 euros;

Application 2: For module or general special-shaped products, the transmission distance between lamp beads is long, because of different wire and transmission distance, the protection of serial connection at both ends of signal and clock line The protective resistance will be slightly different, depending on the actual use.

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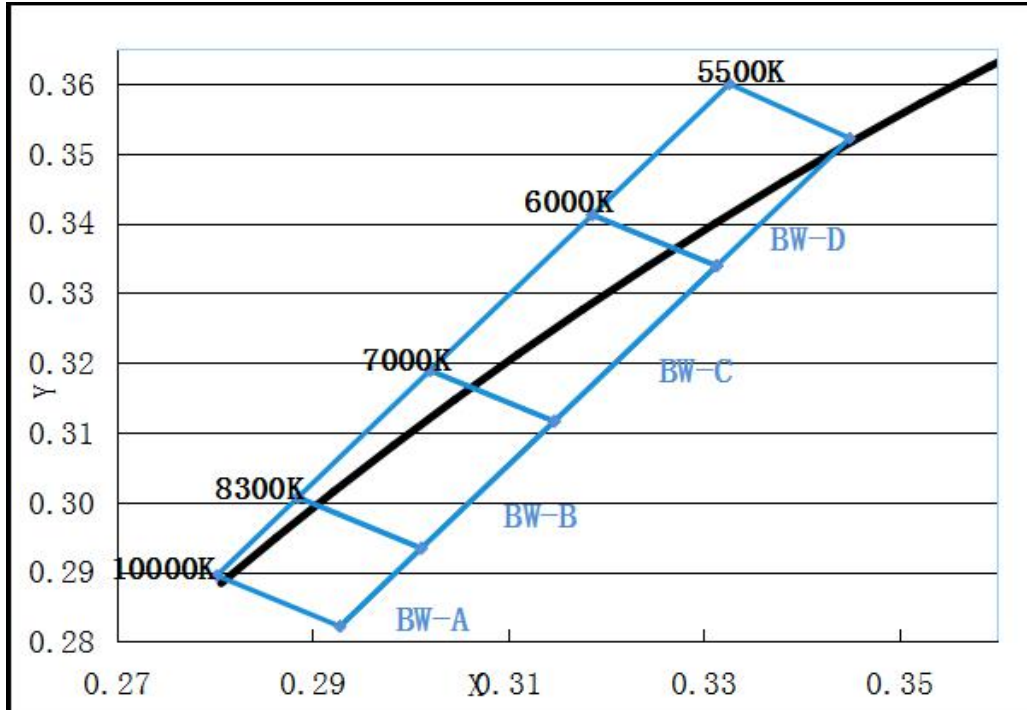
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CIE 1931 Chromaticity Diagram:



Chromaticity Coordinates Specifications for Bin Rank:

Name	X1	Y1	X2	Y2	X3	Y3	X4	Y4
BW-A	0.2928	0.2822	0.2802	0.2895	0.2885	0.3007	0.3011	0.2934
BW-B	0.3011	0.2934	0.2885	0.3007	0.302	0.3189	0.3147	0.3116
BW-C	0.3147	0.3116	0.302	0.3189	0.3186	0.3412	0.3313	0.3339
BW-D	0.3313	0.3339	0.3186	0.3412	0.3326	0.36	0.3449	0.3522

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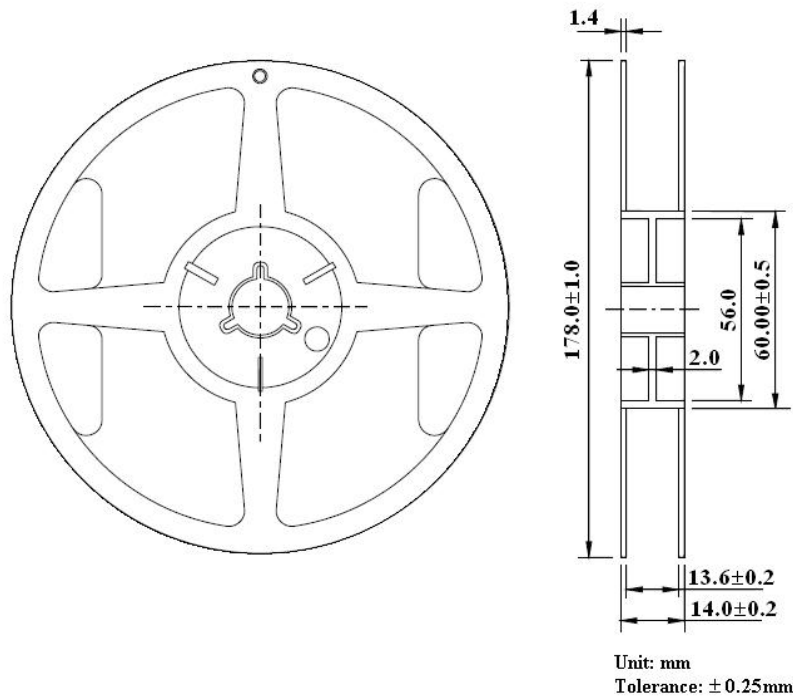
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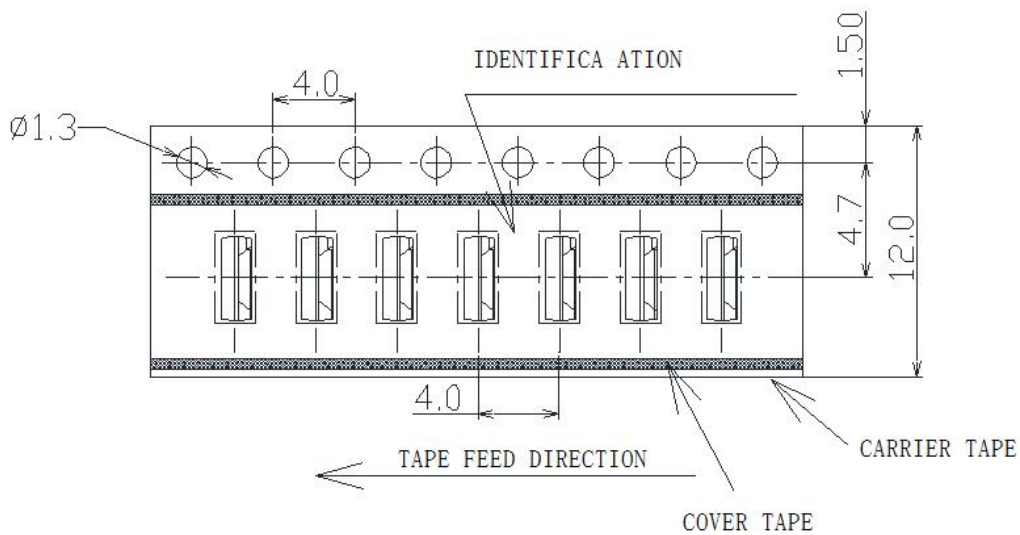
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Reel Dimensions:



Carrier Tape Dimensions:

Loaded quantity 1500 pcs per reel.



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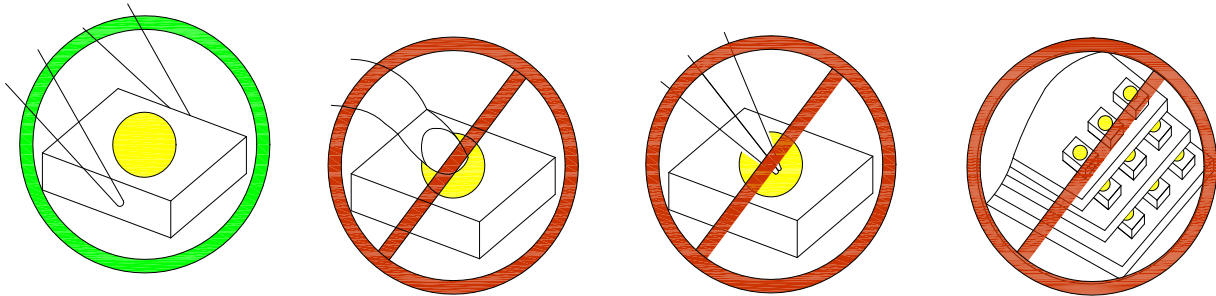
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CAUTIONS

1. Handling Precautions:

- 1.1. Handle the component along the side surfaces by using forceps or appropriate tools.
- 1.2. Do not directly touch or handle the silicone lens surface. It may damage the internal circuitry.
- 1.3. Do not stack together assembled PCBs containing exposed LEDs. Impact may scratch the silicone lens or damage the internal circuitry.



Compare to epoxy encapsulant that is hard and brittle, silicone is softer and flexible. Although its characteristic significantly reduces thermal stress, it is more susceptible to damage by external mechanical force. As a result, special handling precautions need to be observed during assembly using silicone encapsulated LED products. Failure to comply might lead to damage and premature failure of the LED.

2. Storage

- 2.1. Do not open moisture proof bag before the products are ready to use.
- 2.2. Before opening the package, the LEDs should be kept at 30°C or less and 60%RH or less.
- 2.3. The LEDs should be used within a year.
- 2.4. After opening the package, the LEDs should be kept at 30°C or less and 60%RH or less.
- 2.5. The LEDs should be used within 24 hours after opening the package.
- 2.6. If the moisture adsorbent material has fabled away or the LEDs have exceeded the storage time, baking treatment should be performed using the following conditions. Baking treatment: 65±5°C for 24 hours.

3. Soldering Condition

Spec No.: FV4818

Issue No.: G-Rev-4

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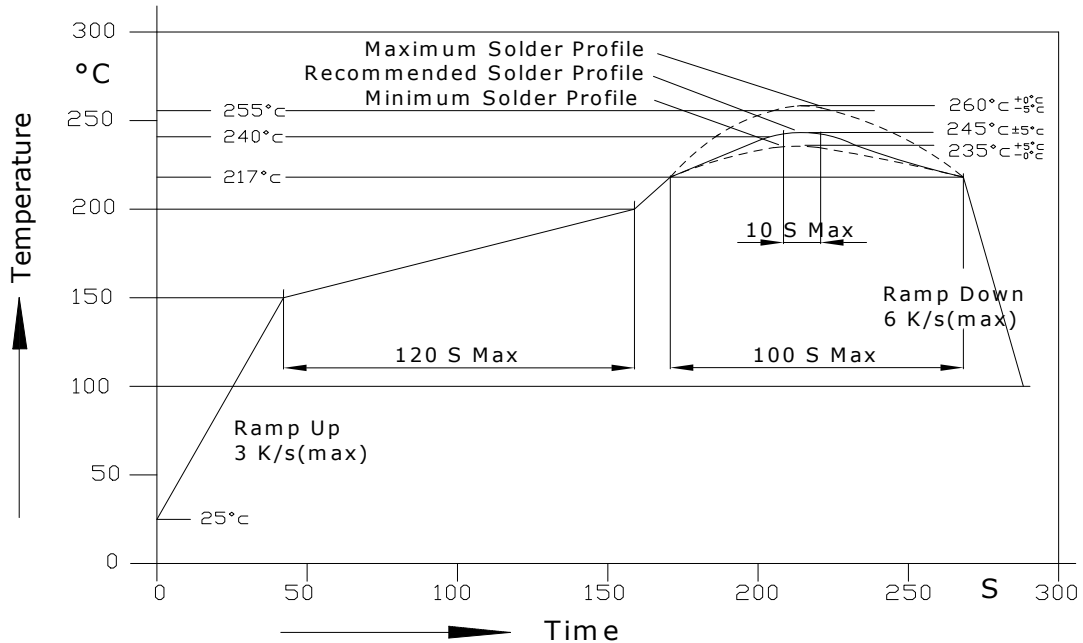
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3.1. Pb-free solder temperature profile



3.2. Reflow soldering should not be done more than two times.

3.3. When soldering, do not put stress on the LEDs during heating.

3.4. After soldering, do not warp the circuit board.

3.5. Recommended soldering conditions:

Reflow soldering		Soldering iron	
Pre-heat	150~200°C	Temperature	300°C Max.
Pre-heat time	120 sec. Max.	Soldering time	3 sec. Max.
Peak temperature	250°C Max.		(one time only)
Soldering time	10 sec. Max.(Max. two times)		

3.6. Because different board designs use different number and types of devices, solder pastes, reflow ovens, and circuit boards, no single temperature profile works for all possible combinations.

However, you can successfully mount your packages to the PCB by following the proper guidelines and PCB-specific

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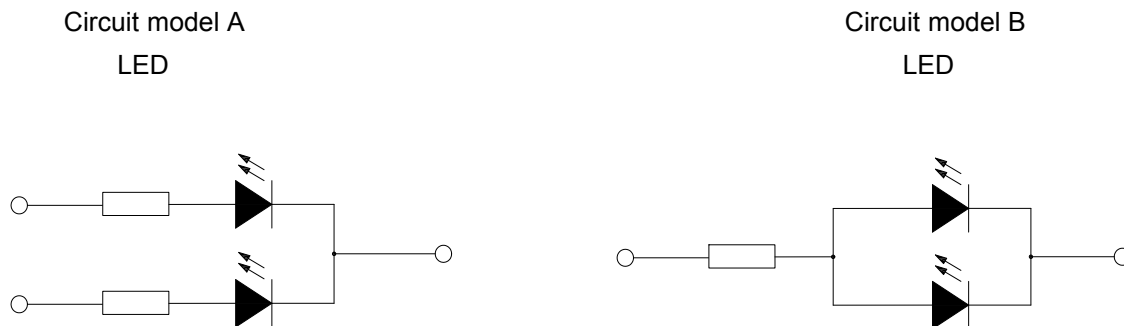
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characterization.

4. Drive Method

4.1. An LED is a current-operated device. In order to ensure intensity uniformity on multiple LEDs connected in parallel in an application, it is recommended that a current limiting resistor be incorporated in the drive circuit, in series with each LED as shown in Circuit A below.



a. Recommended circuit.

b. The brightness of each LED might appear different due to the differences in the I-V characteristics of those LEDs.

5. ESD (Electrostatic Discharge):

Static Electricity or power surge will damage the LED. Suggestions to prevent ESD damage:

- Use of a conductive wrist band or anti-electrostatic glove when handling these LEDs.
- All devices, equipment, and machinery must be properly grounded.
- Work tables, storage racks, etc. should be properly grounded.
- Use ion blower to neutralize the static charge which might have built up on surface of the LED's plastic lens as a result of friction between LEDs during storage and handling.

ESD-damaged LEDs will exhibit abnormal characteristics such as high reverse leakage current, low forward voltage, or “no lightup” at low currents. To verify for ESD damage, check for “lightup” and V_f of the suspect LEDs at low currents. The V_f of “good” LEDs should be $>2.0V@0.1mA$ for InGaN product and $>1.4V@0.1mA$ for AlInGaP product.

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